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				Application Number	TBD	
				Filing Date	Herewith	
				First Named Inventor	Hou, et al.	
				Art Unit	TBD	
(use as many sheets as necessary)			cessary)	Examiner Name	TBD	
Sheet	11	of	1	Attorney Docket Number	TSM03-0151	

U.S. PATENT DOCUMENTS					
Cite No.1	Document Number	Publication Date	Name of Patentee or	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	
	Number - Kind Code <sup>2 (# known)</sup>	MM-DD-YYYY	Applicant of Cited Document		
1	US-6,194,739 B1	02-27-2001	Ivanov, et al.		
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	Cite No. <sup>1</sup>	No. 1 Number - Kind Code <sup>2</sup> (# Amount)	Cite No.¹ Number - Kind Code² (# Annown)  Document Number - Publication Date MM-DD-YYYY	No. Number - Kind Code <sup>2</sup> (# known) MM-DD-YYYY Applicant of Cited Document	

FOREIGN PATENT DOCUMENTS						
Examiner Initials*	Cite No.	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	
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OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS				
Examiner Cite, Initials* No.		Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issuo number(s), publisher, city end/or country where published.	12	
hu	2	HU, C., "SOI and Nanoscale MOSFETS," Plenary Paper, Dept. of Electrical Engineering and Computer Sciences, University of California, Berkeley, CA.		
he	3	YANG, F.L., et al., "25 nm CMOS Omega FETs," IEEE 2002.		
W	4	COLINGE, J.P., et al., "SOI Devices for Sub-0.1µm Gate Lengths," Proc. 23 <sup>rd</sup> International Conference on Microelectronics (MIEL 2002), Vol. 1, NIS, Yugoslavia, May 12-15, 2002, pp. 109-113.		

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